

IN THE CLAIMS:

Kindly replace the claims of record with the following full set of claim:

1. ((Currently amended) A tuner comprising:

a regulating system for regulating, with respect to a reference level, the amplitude level of an amplified signal produced by an amplifier, said regulating system comprising:

attenuation circuitry for generating an attenuated signal from said amplified signal according to a programmable attenuation factor, wherein said attenuation means comprise:

a network of resistances defined by a set of π -structures connected in series, each of said π -structures including at least one parallel resistor element in connection with a series resistor element, wherein a resistance of said parallel resistors and said series resistor in each of said π -structures is selected dependent upon a desired attenuation factor of said π -structure and a polarization resistance;

conversion circuitry for converting said attenuated signal in order to generate an output signal for comparison with said reference level, wherein said conversion circuitry generates said output signal with a level proportional to the square of the effective value of said attenuated signal,

a comparator for forming a difference signal between said output signal and said reference signal; and

means for directly controlling the amplitude level of the amplified signal using the difference signal; and

a demodulator for demodulating said amplified signal.

2. (Currently amended) A regulating system as claimed in claim 1, wherein ~~said attenuation means comprise a network of resistances defined by a set of π -structures connected in series,~~ each node of the π -structures being connected to a switch for defining said programmable attenuation factor.
3. (Previously presented) A regulating system as claimed in claim 2, wherein the switches are activated by a command word delivered by a digital bus.
- 4 (Previously presented) A regulating system as claimed in claim 1, comprising a voltage comparator including an adjustable voltage/current converter, for generating an output current signal I_{AGC} being proportional to the difference between said output signal and said reference level.
5. (Previously presented) An integrated circuit comprising a regulating system as claimed in claim 1.
6. (cancelled).
7. (New) The regulating system as claimed in claim 1, wherein an equivalent impedance seen at each of said π -structures is equal to the equivalent impedance of said network of resistances as seen from an output.

8. (New) The regulating system as claimed in claim 1, wherein the resistances of said parallel and series resistors are further selected such that a ratio of the parallel resistor to the series resistor is equal to a predetermined value.